Parallel Neuro-Tabu Search Algorithm for the Job Shop Scheduling Problem

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Abstract. We propose two parallel algorithms based on neuro-tabu search method, designed to solve the jobs shop problem of scheduling. The fist algorithm is based on independent runs of the neuro-tabu with different starting points. The second one uses sophisticated diversification method based on path-relinking methodology applied to the set of elite solutions. Proposed approaches are especially effective for the instances of large size.

1 Introduction

We consider the job shop scheduling problem here, which can be described as follows (see [4]). There is a set of jobs and a set of machines. Each job consists of a number of operations which have to be processed in a given order, each one on a specified machine during a fixed time. The processing of an operation cannot be interrupted. Each machine can process at most one operation at a time. We want to find a schedule (the assignment of operations to time intervals on machines) that minimizes the *makespan*. The job shop scheduling problem, although relatively easily stated, is strongly NP-hard and it is considered as one of the hardest problems in the area of combinatorial optimization.

Because of NP-hardness of the problem heuristics and metaheuristics are recommended as 'the most reasonable' solution methods. The majority of these methods refer to the makespan minimization. We mention here a few recent studies: Jain. Rangaswamy. and Meeran [21]: Pezzella and Merelli [30]: Grabowski and Wodecki [17]: Nowicki and Smutnicki [27]: Bożejko and Uchroński [6]. Heuristics algorithms based on dispatching rules are also proposed in papers of Holthaus and Rajendran [19]. Bushee and Svestka [10] for the problem under consideration. For the other regular criteria such as the total tardiness there are proposed

L. Rutkowski et al. (Eds.): ICAISC 2013, Part II, LNAI 7895, pp. 489-499, 2013. © Springer-Verlag Berlin Heidelberg 2013

metaheuristics based on various local search techniques: simulated annealing [34], tabu search [2] and genetic search [26].

Here we propose the new approach to the distributed tabu search metaheurs tic designing to solve difficult discrete optimization problems, such as the shop problem, using a multi-GPU cluster with distributed memory. We also termine theoretical number of processors, for which the speedup measure has maximum value. We experimentally determine what parallel execution time can be obtained in real-world installations of multi-GPU clusters (nVidia Tes S2050 with a 6-cores CPU server) for Taillard benchmarks [32] of the job shocheduling problem, and compare them with theoretically determined values.

2 Job Shop Problem

Job shop scheduling problems result from many real-world cases, which methat they have good practical applications as well as industrial significance. Let us consider a set of jobs $\mathcal{J} = \{1, 2, \ldots, n\}$, a set of machines $M = \{1, 2, \ldots, n\}$ and a set of operations $\mathcal{O} = \{1, 2, \ldots, o\}$. The set \mathcal{O} is decomposed into subsconnected with jobs. A job j consists of a sequence of o_j operations index consecutively by $(l_{j-1}+1, l_{j-1}+2, \ldots, l_j)$, which have to be executed in the order, where $l_j = \sum_{i=1}^j o_i$ is the total number of operations of the first j job $j=1,2,\ldots,n,\ l_0=0,\ \sum_{i=1}^n o_i=o$. An operation i has to be executed machine $v_i\in M$ without any idleness in time $p_i>0,\ i\in\mathcal{O}$. Each machine execute at most one operation at a time. A feasible solution constitutes a vector of times of the operation execution beginning $S=(S_1,S_2,\ldots,S_o)$ such that the following constraints are fulfilled

$$S_{l_{j-1}+1} \ge 0, \quad j = 1, 2, \dots, n.$$

$$S_i + p_i \le S_{i+1}, \quad i = l_{j-1} + 1, \quad l_{j-1} + 2, \dots, l_j - 1, \quad j = 1, 2, \dots, n,$$

$$S_i + p_i \le S_j$$
 or $S_j + p_j \le S_i$, $i, j \in O$, $v_i = v_j$, $i \ne j$.

Certainly, $C_j = S_j + p_j$. An appropriate criterion function has to be added to the above constraints. The most frequent are the following two criteria: minimization of the time of finishing all the jobs and minimization of the sum of job finishing times. From the formulation of the problem we obtain $C_j \equiv C_{l_j}$, $j \in \mathcal{J}$.

The first criterion, the time of finishing all the jobs

$$C_{\max}(S) = \max_{1 \le j \le n} C_{l_j}. \tag{4}$$

corresponds to the problem denoted as $J||C_{\text{max}}$ in the literature. The second criterion, the sum of job finishing times

$$C(S) = \sum_{j=1}^{n} C_{l_{j}}.$$
 (5)

corresponds to the problem denoted as $J||\sum C_i$ in the literature.

Both problems described are strongly NP-hard and although they are similarly delled, the second one is found to be harder because of the lack of some specific perties (so-called block properties, see [27]) used in optimization of execution of solution algorithms.

Tabu Search Mechanism with Neural Network Application

In the considered neuro-tabu search algorithm NTS each move is represented by some neuron. For the neighborhood considered in [28] a network of neurons formed of o-1 neurons. Let i-th neuron represents a move consisting in swap of two adjacent elements on the positions i and i+1 in a solution π .

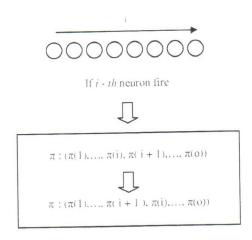


Fig. 1. Dependencies between activation of the neuron and a move

In a proposed neural network architecture a history of each neuron is stored as its internal state (tabu effect). If in an iteration neuron is activated, then the value 1 is fixed on its output and values 0 are fixed on the outputs of other neurons. The neuron activated in an iteration must not be activated once again for the next s iterations. Each neuron is defined by the following equations:

$$\eta_i(t+1) = \alpha \Delta_i(t). \tag{6}$$

$$\Delta_i(t) = \frac{C_{max}(\pi_v^{(t)}) - C_{max}^*}{C_{max}^*}.$$
 (7)

$$\gamma_i(t+1) = \sum_{d=0}^{s-1} k^d x_i(t-d), \tag{8}$$

where $x_i(t)$ is an output of the neuron i in the iteration t. Symbol $C_{max}(\pi_v^{(t)})$ means the value of the goal function for the permutation obtained after executing

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a move v in the iteration t, i.e. $\pi^{(t)}$. Symbol $\Delta_i(t)$ means a normalized, current value of the goal function, and C^*_{max} is the value of the best solution found so far. Parameters α i k are scale factors. A symbol $\eta_i(t+1)$ (gain effect) defines quality of a move v. A variable $\gamma_i(t+1)$ (tabu effect) stores a history of the neuron i for the last s iterations. Neuron is activated if it has a low value of the tabu effect and it gives a better reduction of the C_{max} . More detailed a neuron i is activated if it has the lowest $\{\eta_i(t+1) + \gamma_i(t+1)\}$ value of all the neurons.

If 0 < k < 1 i s = t then an equation(8) takes the form of:

$$\gamma_i(t+1) = k\gamma_i(t) + x_i(t). \tag{9}$$

where $\gamma_i(0) = 0$ and $x_i(0) = 0$ for each i. From the equation (9) it follows, that the value of $\gamma_i(t)$ of each neuron decreases exponentially (Fig. 3 and 4).

In many algorithms proposed in the literature which are based on the tabu search method a so-called aspiration criterion is implemented. It consists in executing of the forbidden move if it follows to the base solution with the goal function value lower than the best found so far.

In the proposed neuro-tabu search such a function can be implemented by ignoring tabu effect for a move v for which $\Delta_i < 0$. However during computational experiments we have observed that it does not give good effects. Therefore a proposed neuro-tabu search has not got such a function. The skeleton of the neuro-tabu search algorithm is given on the Fig. 2.

4 Parallel Neuro-Tabu Search Algorithm pNTS

Here we propose a solution method to the job shop problem in the distributed computing environments, such as multi-GPU clusters. Tabu search algorithm is executed in concurrent working threads, as in *multiple-walk* model of parallelization [1] (MPDS, *Multiple starting Point Different Strategies* in the Voß [33 classification of parallel tabu search metaheuristic). Additionally, MPI library is used to distribute calculations among GPUs (see Fig. 4).

Now let us consider a single cycle of the MPI data broadcasting, multi-GPU computations and batching up of the results obtained. Let us assume that the single communication procedure between two nodes of a cluster takes the time T_{comm} , the time of sequential tabu search computations is T_{seq} and the computations time of parallel tabu search is $T_{calc} = \frac{T_{seq}}{p}$ (p is the number of GPUs). Therefore, the total parallel computations time of the single cycle is

$$T_p = 2T_{comm}\log_2 p + T_{calc} = 2T_{comm}\log_2 p + \frac{T_{seq}}{p}.$$

In case of using more processors, the parallel computing time $\left(\frac{T_{seq}}{p}\right)$ decreases whereas the time of communication $(2T_{comm}\log p)$ increases. We are looking for such a number of processors p (let us call it p^*) for which T_p is minimal. By calculating $\frac{\partial T_p}{\partial p}=0$ we obtain

$$\frac{2T_{comm}}{p\ln 2} - \frac{T_{seq}}{p^2} = 0 \tag{10}$$

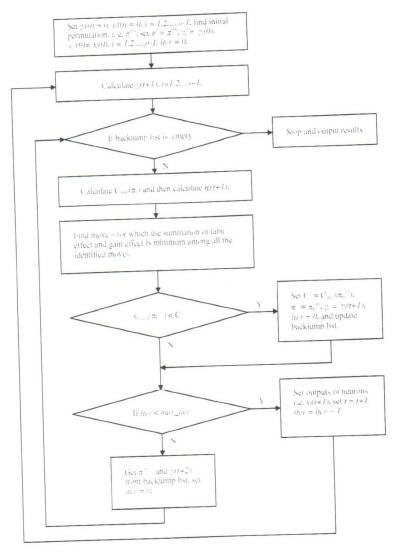


Fig. 2. A skeleton of the neuro-tabu search algorithm

and then

$$p = p^* = \frac{T_{seq} \ln 2}{2T_{comm}}. (11)$$

which provides us with an optimal number of processors p^* which minimizes the value of the parallel running time T_p .

The fraction of communication time is $O(\log_2 p)$ in this tree-based data broadcasting method, therefore this is another situation than for linear-time broadcasting (discussed in [3]), for which the overall communication and calculation

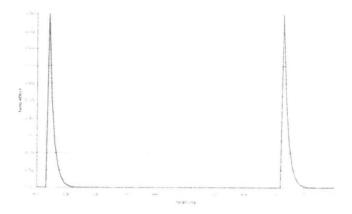


Fig. 3. Changes of the $\gamma(t)$ value for k=0.5

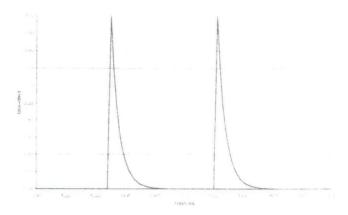


Fig. 4. Changes of the $\gamma(t)$ value for k=0.7

efficiency is much lower. On the other hand the linear-time broadcasting is similar to described by Brooks' Law [9] for project management, i.e. the expected advantage from splitting development work among n programmers is O(n) but the communications cost associated with coordinating and then merging their work is $O(n^2)$.

5 Advanced Neuro-Tabu Search Algorithm iNTS

As the second solution method we propose an approach introduced by Nowicki and Smutnicki [27] with using neuro-tabu instead of classic tabu search algorithm, as in the original paper. The proposed iNTS algorithm operates on the set of dispersed (elite) solution obtained with using $NIS(\gamma, \delta, C^R)$ function (see Fig. 6), where γ, δ are two processing orders and C^R is the reference makespan (goal function value). Inside, the neighborhood generation function is used based

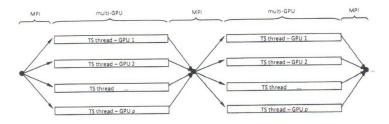


Fig. 5. Skeleton of the Multi-Level Tabu Search metaheuristic

on swap moves of adjacent operations (see Bożejko [4]). We denote by $N(\pi)$ the set of moves obtained from a solution π by swapping any two adjacent operations lying on critical path (to obtain feasible solution, see [17]). The fundamental aim of the NIS function is to provide the solution $\varphi = NIS(\gamma, \delta, C^R)$ located 'between' γ and δ reference solutions to run a neuro-tabu search explaration in the main iNTS algorithm (see Fig. 7). To control the distance between any two solution α, β , the Kendall's tau measure $D(\alpha, \beta)$ is used to represent the minimal number of adjacent swap moves (inversions) to obtain permutation β^{-1} from α^{-1} (for more measures on permutations see e.g. Diaconis [13]). Values of tuning parameters were following: maxE = 8 (number of elite solutions), maxD = 5 (maximal distance used in the loop), maxV = 0.5.

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Algorithm 1. NIS(\gamma, \delta, C^R)

Input: \gamma, \delta - two processing orders; C^R - reference makespan;

Output: \varphi - processing order; update reference makespan C^R;

\pi \leftarrow \gamma; iter \leftarrow 0. Find \delta^{-1} and D(\gamma, \delta)

repeat

iter \leftarrow iter + 1; Find N(\pi);

For any v \in N(\pi) calculate and store C_{max}(\pi_{(v)});

Find N^+ = \{v = (x, y) \in N(\pi) : \delta^{-1}(y) < \delta^{-1}(x)\};

if N^+ \neq \emptyset than K \leftarrow N^+ else K \leftarrow N(\pi);

Select the move w \in K such, that

C_{max}(\pi_{(w)}) = \min_{v \in K} C_{max}(\pi_{(v)});

Denote \pi_{(w)} by \alpha;

\pi \leftarrow \alpha; \varphi \leftarrow \pi;

if C_{max}(\pi) < C^R than C^R \leftarrow C_{max}(\pi) and exit;

until iter \geq maxV \cdot D(\gamma, \delta); \{maxV \in (0, 1) - parameter\}
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Fig. 6. $NIS(\gamma, \delta, C^R)$ function

6 Computational Experiments

Proposed algorithms were ran on the server based on 6-cores Intel Core i7 CPU X980 (3.33GHz) processor equipped with nVidia Tesla S2050 GPU (1792 cores) working under 64-bit Linux Ubuntu 10.04.4 LTS operating system and tested on the benchmark problems taken from Taillard [32].

```
Algorithm 2. iNTS
Input: \pi^0 - processing orders provided by INSA:
Output: \pi^* - the best found processing order and its makespan C^*:
Set (\pi^1, C^1) \leftarrow NTS(\pi^0) and C^* \leftarrow C^1;
for i \leftarrow 2, \ldots, maxE do \varphi \leftarrow NIS(\pi^{i-1}, \pi^0, C^*); (\pi^i, C^i) \leftarrow NTS(\varphi); C^* = \min\{C^*, C^i\}; repeat

Find 1 \leq l \leq maxE so that D(\pi^k, \pi^l) = \max\{D(\pi^k, \pi^i) : 1 \leq i \leq maxE\}; Set \varphi \leftarrow NIS(\pi^k, \pi^l, C^*) and (\pi^l, C^l) \leftarrow NTS(\varphi); if C^l < C^k than set (\pi^*, C^*) \leftarrow (\pi^l, C^l) and k \leftarrow l; until \max\{D(\pi^k, \pi^l) : 1 \leq i \leq maxE\} < maxD.
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Fig. 7. Algorithm iNTS

Computational experiments results are compared in the Table 1. Particular columns have the following notion:

- $-\ sNTS$ sequential Neuro Tabu Search algorithm of Bożejko and Uchroński [6],
- -pNTS parallel (for p=16) Neuro Tabu Search algorithm. MPSS model (due to the Voß [33] classification) without communication (proposed in the Section 4): for each processor the starting solution was generated by the NTS algorithm executed for $process_id*100$ iterations.
- *iNTS* advanced *NTS* algorithm based on the diversification and intensification methodology proposed in the Section 5.

Processing times for all benchmark instances were: (sequential) NTS-132m27.319s, (parallel) pNTS (p=4) – 169m45.748s (longer time of parallel algorithm work causes of the method of starting solutions generation and synchronization). iNTS – about 60 hours. As we can observe the proposed iNTS algorithm managed to obtain the average relative percentage deviation from the

average		0.7596	0.5515	0.2839
TA71-80	100×20	0.0090	0.0090	0.0090
TA61-70	50×20	0.1479	0.0210	0.0140
TA51-60	50×15	0.0915	0.0915	0.0520
TA41-50	30×20	1.8565	1.5634	0.8328
TA31-40	30×15	1.0592	0.7965	0.3764
TA21-30	20×20	1.2486	0.7033	0.4803
TA11-20	20×15	1.1691	0.9129	0.4412
TA01-10	15×15	0.4948	0.3141	0.0652
problem	$n \times m$	sNTS	pNTS(p = 16)	iNTS

Table 1. Percentage relative deviations (PRD) to the best known solutions

best known solution of the Taillard instances on the level of 0.28%. Average PRD values for test instances of the group TA61-TA70 (1,000 operations) are 10 times lower than for the proposed iNTS algorithm. Comparing results for sNTS and pNTS algorithm we can notice that using parallel algorithm results in obtaining much lower PRD values. Based on this we plan to implement parallel version of iNTS algorithm as a future work.

7 Conclusions

In this paper we propose an approach designed to solve difficult problems of combinatorial optimization in distributed parallel architectures without shared memory, such as clusters of nodes equipped with GPU units (i.e. multi-GPU clusters). The methodology can be especially effective for large instances of hard to solve optimization problems, such as flexible scheduling problems as well as discrete routing and assignment problems.

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